PQ2CF1

TO-220 Package, Step Up Output Chopper Regulator

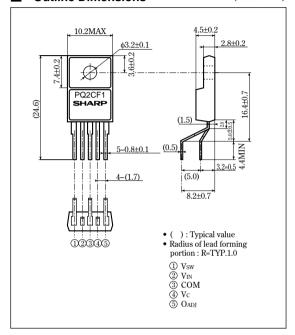
Features

- Maximum switching current: 2.5A
- Built-in soft start function
- Built-in oscillation circuit (oscillation frequency: TYP. 50kHz)
- Built-in overheat protection, overcurrent protection function
- Variable output voltage (4.5 to 35V)
 [Possible to choose step up output / flyback method according to external connection circuit]

Applications

- Personal computers / Word processors
- Printers
- Switching power supplies
- Facsimiles

Outline Dimensions (Unit : mm)



Absolute Maximum Ratings

(Ta=25°C)

Parameter	Symbol	Rating	Unit
*1 Input voltage	Vin	35	V
*2 Switching voltage	Vsw	35	V
Error input voltage	Vadj	7	V
*3 ON/OFF control voltage	Vc	7	V
Switching current	Isw	2.5	Α
Power dissipation (No heat sink)	P _{D1}	1.5	W
Power dissipation (With infinite heat sink)	P _{D2}	15	W
*4 Junction temperature	Tj	150	°C
Operating temperature	Topr	-20 to +80	°C
Storage temperature	Tstg	-40 to +150	°C
Soldering temperature	Tsol	260 (For 10s)	°C

^{\$1} Voltage between V_{IN} terminal and COM terminal

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^{*2} Voltage between Vsw terminal and COM terminal

^{*3} Voltage between Vc terminal and COM terminal

^{**4} Overheat protection may operate at 125<=Tj<=150°C.

[·] Please refer to the chapter " Handling Precautions ".

■ Electrical Characteristics

(Unless otherwise specified, conditions shall be V_{IN}=5V,Io=0.2A,Vo=12V, T_a=25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output saturation voltage	VSAT	Isw=2A	_	0.6	1.2	V
Reference voltage	V _{ref}	_	1.235	1.26	1.285	V
Reference voltage temperature fluctuation	ΔV_{ref}	T _j =0 to 125°C	_	±0.5	_	%
Load regulation	RegL	Io=70 to 570mA	-	0.1	1.5	%
Line regulation	RegI	V _{IN} =3.5 to 10V	-	0.2	1.5	%
Efficiency	η	Io=0.5A	-	85	_	%
Oscillation frequency	fo	_	40	50	60	kHz
Oscillation frequency temperature fluctuation	Δfo	T _j =0 to 125°C	I	±5	_	%
Maximum duty	DMAX	⑤ terminal is open	90	_	_	%
Over current detecting level	IL	Duty=50%,	2.7	4.4	5.8	A
Charge current 1	Ichg1	④ terminal=0V, ④ terminal	-80	-50	-20	μΑ
Charge current 2	Ichg2	4 terminal=0.5V, 4 terminal	-150	-100	-50	μΑ
Input threshold voltage	V _{THL}	Duty=0%, ④ terminal	0.55	0.75	0.95	V
Vc terminal low level voltage	Vch	① terminal is open, ⑤ terminal=1.1V	1.65	1.85	2.05	V
Vc terminal high level voltage	Vcl	① terminal is open, ⑤ terminal=1.4V	0.3	0.45	0.6	V
On threshold voltage	V _{THON}	① terminal is open, ④ terminal	0.1	0.2	0.3	V
Stand-by current	Isd	V _{IN} =35V, ④ terminal=0V,No L,Co,	_	270	400	μА
		D,R1,R2				
Output OFF-state dissipation current	Iqs	V _{IN} =35V, ④ terminal=0.5V,No L,		4.0	12	mA
		Co,D,R1,R2				

Block Diagram

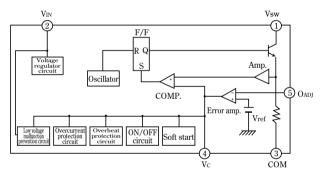
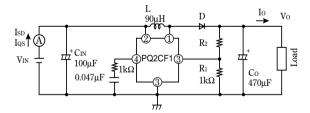
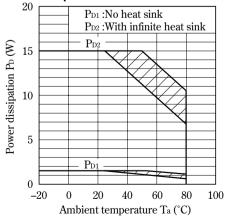


Fig. 1 Test Circuit



L: HK-12S100-9000 (made by Toho Co.) D: ERC80-004 (made by Fuji electronics Co.)

Fig. 2 Power Dissipation vs. Ambient Temperature



Note) Oblique line portion: Overheat protection may operate in this area.

Fig. 3 Overcurrent Protection Characteristics

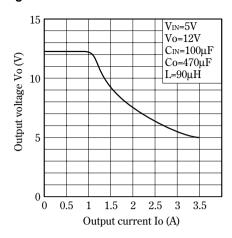


Fig. 5 Reference Voltage Fluctuation vs. Junction Temperature

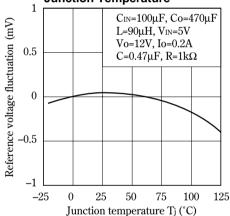


Fig. 7 Line Regulation vs. Input Voltage

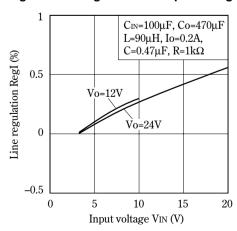


Fig. 4 Efficiency vs. Input Voltage

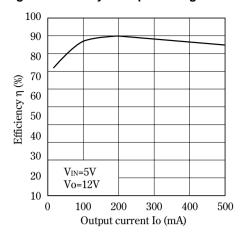


Fig. 6 Load Regulation vs. Output current

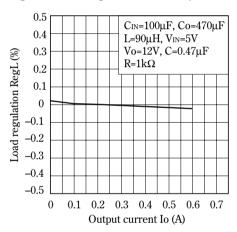


Fig. 8 Oscillation Frequency Fluctuation vs. Junction Temperature

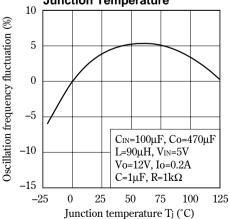
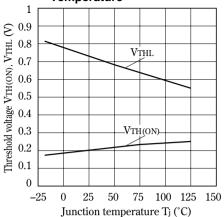
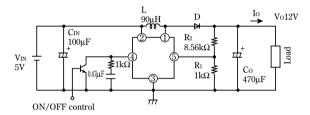


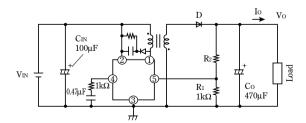
Fig. 9 Threshold Voltage vs. Junction Temperature



■ Step - Up Type Circuit Diagram (12V Output)



■ Flyback Method Circuit Diagram



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